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PAGE 2/5 * RCVD AT 1/23/2004 2:19:59 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-2/3 * DNIS:7469195 * CSID: * DURATION (mm-ss):01-40-

In re Patent Application of:
KUMAR ET AL.
Serial No. 10/611,322
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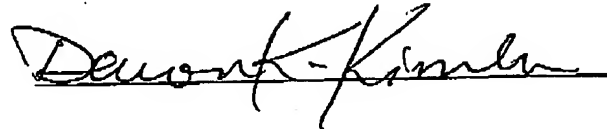
application. If any fee is believed to be necessary,
authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,


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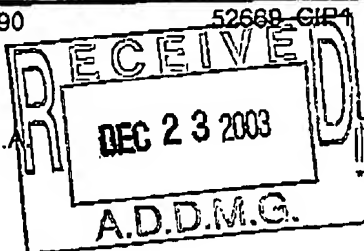


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CONFIRMATION NO. 4030

UPDATED FILING RECEIPT



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Date Mailed: 12/19/2003

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Assignment For Published Patent Application

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Foreign Applications

INDIA 635/DEL/2002 06/13/2002
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Non-Publication Request: No

Early Publication Request: No

Title

Digital electronic circuit for translating high voltage levels to low voltage levels

Preliminary Class

327

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